

Reliability Test Result

DEVICE : BU7271S Family

RESULT

ITEM	METHOD	CONDITION	n (pcs)	pn (pcs)
Resistance to Soldering Heat (#1)	Soldering heat stress (IR reflow : peak 260°C)	—	22 x 3	0
Resistance to Soldering Heat (#1)	Soak into solder tub 260±3°C	10sec	22 x 3	0
Temperature Cycling (*1)	Tstg min (30min) / Tstg max (30min)	100cycles	22 x 3	0
Steady State Operating Life Test	Apply the specified voltage at Topr max	1000hours	22 x 3	0
High Temperature Storage	Storage at Tstg max	1000hours	22 x 3	0
Low Temperature Storage	Storage at Tstg min	1000hours	22 x 3	0
Temperature Humidity Storage (*1)	Place at Ta=85°C / RH=85%	1000hours	22 x 3	0
Temperature Humidity Bias (*1)	Apply the specified voltage at Ta=85°C / RH=85%	1000hours	22 x 3	0
Pressure Cooker (*1)	Storage at Ta=121°C / RH=100% / 2atm	100hours	22 x 3	0
Electro Static Discharge (HBM)	C=100pF / R=1.5kΩ, ±2000V	—	5	0
Electro Static Discharge (MM)	C=200pF / R=0Ω, ±200V	—	5	0
Latch-up	Current Latch up ±200mA	—	5	0

- Criterion at each test : To satisfy product specification.
- Objective packages are SSOP5-CG .

(#1) Preconditioning

Bake, Ta=125°C → Soak, Ta=85°C / RH=85% / 168h

(#2) Preconditioning

Bake, Ta=125°C → Soak, Ta=85°C / RH=85% / 168h → Reflow, Peak=260°C

Notes

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