

Parameter	Value
$V_{CC}$	50V
$I_{C(MAX.)}$	100mA
$R_1$	47k $\Omega$
$R_2$	22k $\Omega$

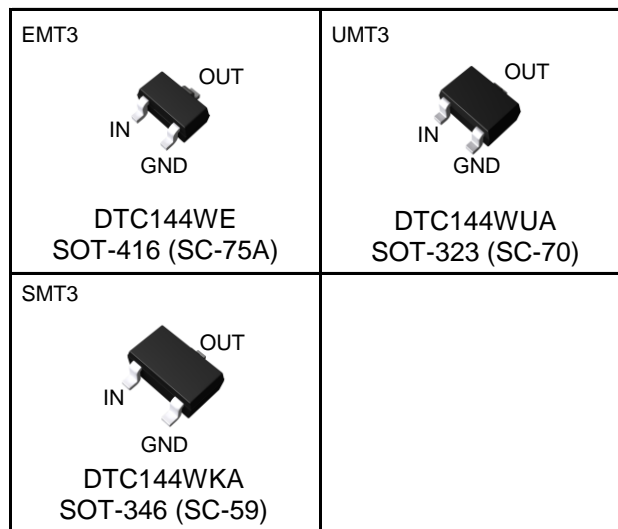
### ●Features

- 1) Built-In Biasing Resistors
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- 4) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 5) Complementary PNP Types :DTA144W series
- 6) Lead Free/RoHS Compliant.

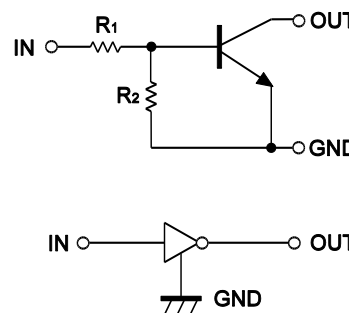
### ●Application

Switching circuit, Inverter circuit, Interface circuit, Driver circuit

### ●Outline



### ●Inner circuit



### ●Packaging specifications

Part No.	Package	Package size (mm)	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit (pcs)	Marking
DTC144WE	EMT3	1616	TL	180	8	3,000	86
DTC144WUA	UMT3	2021	T106	180	8	3,000	86
DTC144WKA	SMT3	2928	T146	180	8	3,000	86

**●Absolute maximum ratings (Ta = 25°C)**

Parameter		Symbol	Values	Unit
Supply voltage		$V_{CC}$	50	V
Input voltage		$V_{IN}$	-10 to +40	V
Output current		$I_O$	30	mA
Collector current		$I_{C(MAX.)}^{*1}$	100	mA
Power dissipation	DTC144WE	$P_D^{*2}$	150	mW
	DTC144WUA DTC144WKA		200	mW
Junction temperature		$T_j$	150	°C
Range of storage temperature		$T_{stg}$	-55 to +150	°C

**●Electrical characteristics (Ta = 25°C)**

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Input voltage	$V_{I(off)}$	$V_{CC} = 5V, I_O = 100\mu A$	-	-	0.8	V
	$V_{I(on)}$	$V_O = 0.3V, I_O = 2mA$	4	-	-	
Output voltage	$V_{O(on)}$	$I_O / I_I = 10mA / 0.5mA$	-	0.1	0.3	V
Input current	$I_I$	$V_I = 5V$	-	-	0.16	mA
Output current	$I_{O(off)}$	$V_{CC} = 50V, V_I = 0V$	-	-	0.5	$\mu A$
DC current gain	$G_I$	$V_O = 5V, I_O = 5mA$	56	-	-	-
Input resistance	$R_1$	-	32.9	47	61.1	k $\Omega$
Resistance ratio	$R_2/R_1$	-	0.37	0.47	0.57	-
Transition frequency	$f_T^{*1}$	$V_{CE} = 10V, I_E = -5mA,$ $f = 100MHz$	-	250	-	MHz

\*1 Characteristics of built-in transistor

\*2 Each terminal mounted on a reference footprint

●Electrical characteristic curves(Ta = 25°C)

Fig.1 Input voltage vs. output current (ON characteristics)

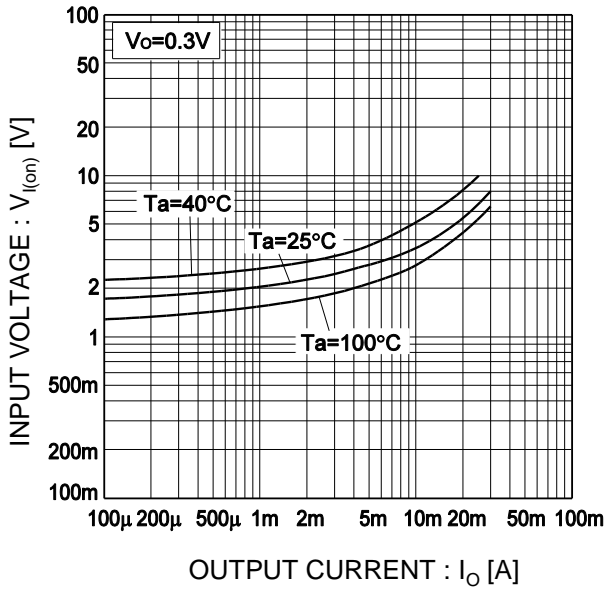


Fig.2 Output current vs. input voltage (OFF characteristics)

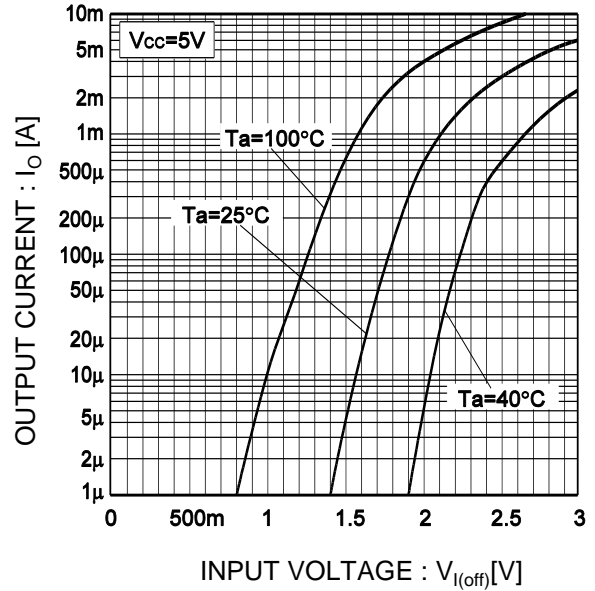


Fig.3 Output current vs. output voltage

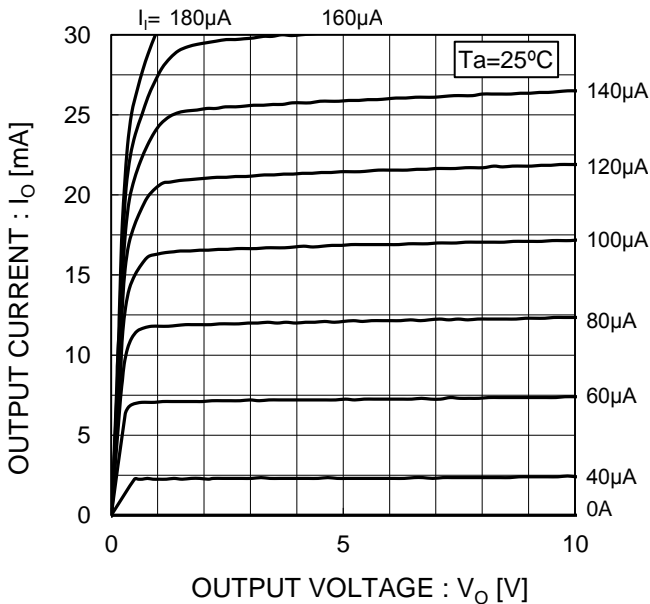
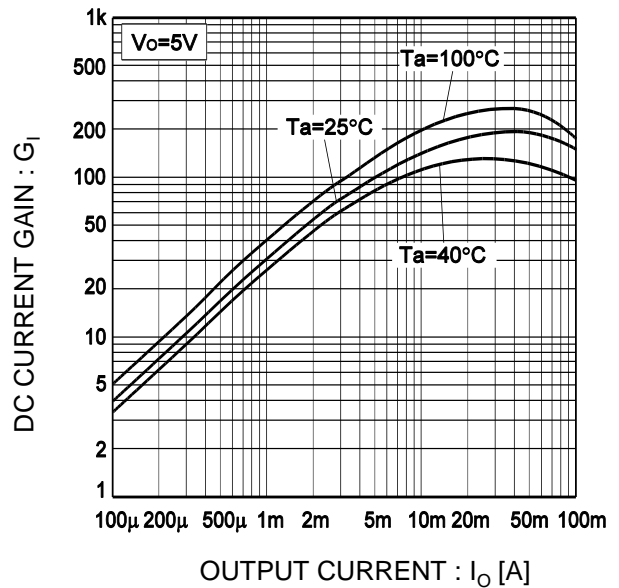
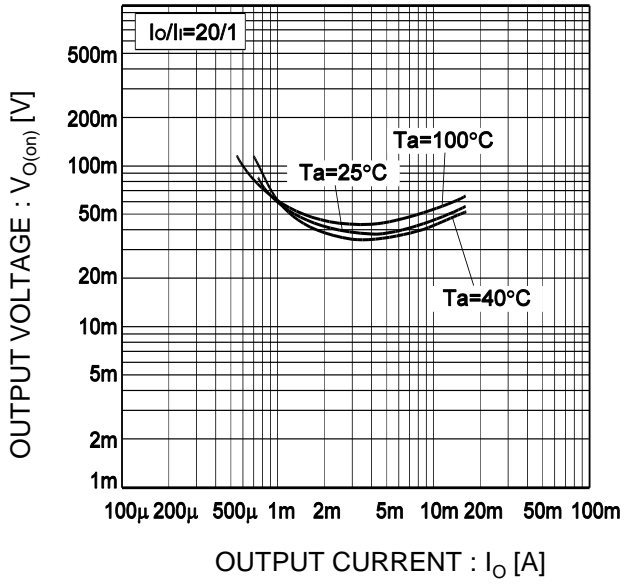


Fig.4 DC current gain vs. output current

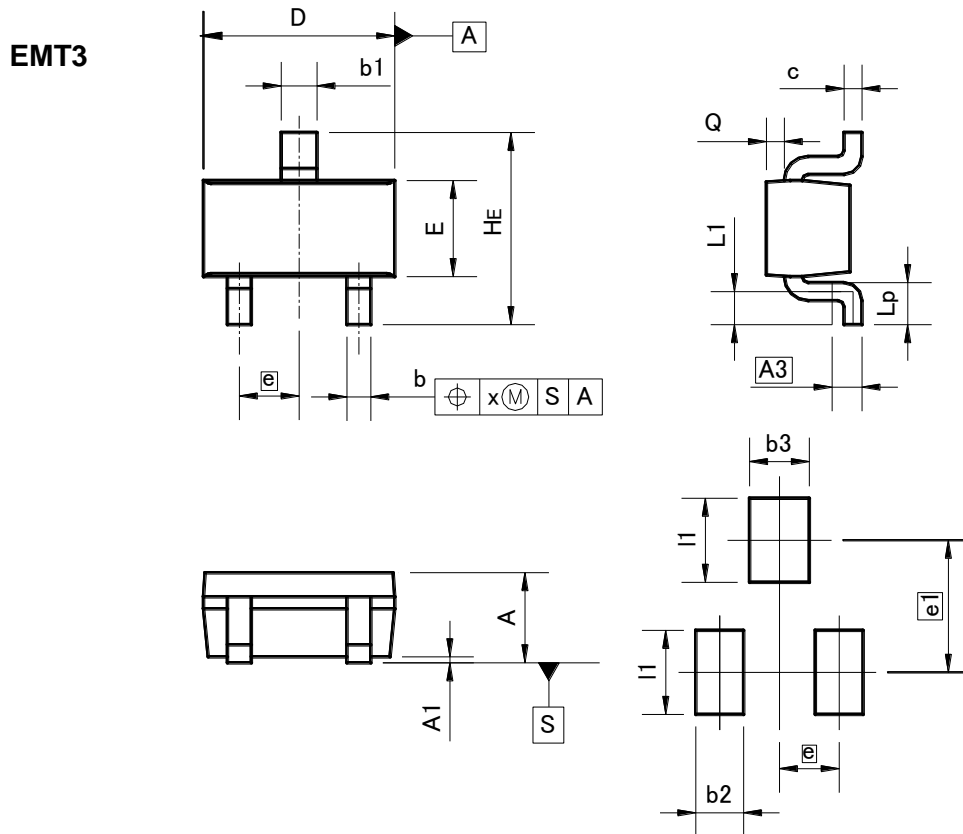


●Electrical characteristic curves(Ta = 25°C)

Fig.5 Output voltage vs. output current



●Dimensions (Unit : mm)



Pattern of terminal position areas

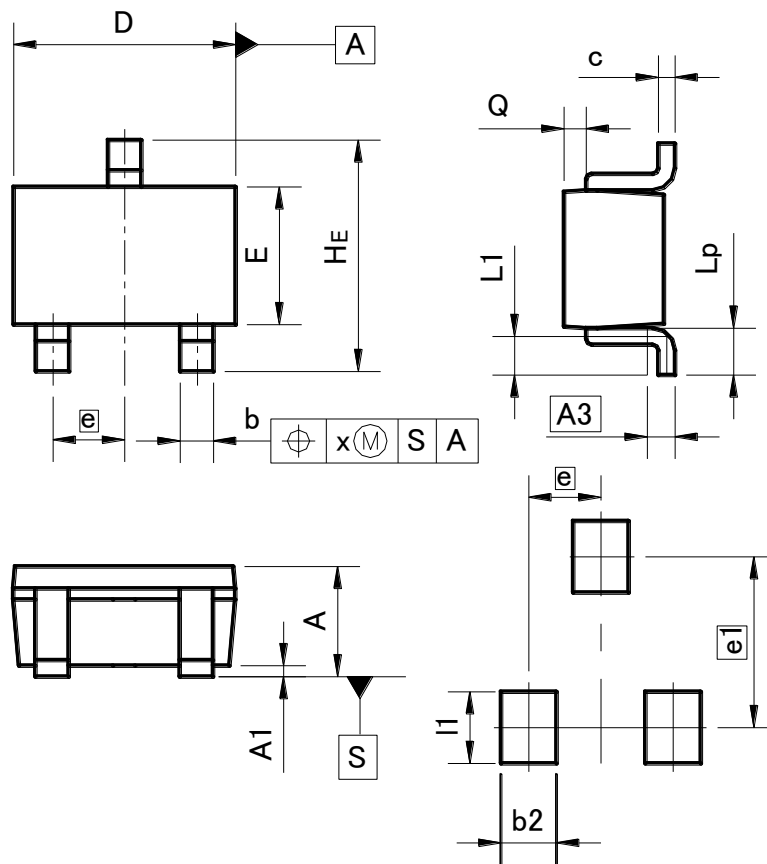
DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.60	0.80	0.024	0.031
A1	0.00	0.10	0	0.004
A3	0.25		0.01	
b	0.15	0.30	0.006	0.012
b1	0.25	0.40	0.01	0.016
c	0.10	0.20	0.004	0.008
D	1.50	1.70	0.059	0.067
E	0.70	0.90	0.028	0.035
e	0.50		0.02	
HE	1.40	1.80	0.055	0.071
L1	0.10	-	0.004	-
Lp	0.15	-	0.006	-
Q	0.05	0.25	0.002	0.01
x	-	0.10	-	0.004

DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
e1	1.10		0.04	
b2	-	0.40	-	0.016
b3	-	0.50	-	0.02
l1	-	0.70	-	0.028

Dimension in mm/inches

●Dimensions (Unit : mm)

UMT3



Pattern of terminal position areas

DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.80	1.00	0.031	0.039
A1	0.00	0.10	0	0.004
A3	0.25		0.01	
b	0.15	0.30	0.006	0.012
c	0.10	0.20	0.004	0.008
D	1.90	2.10	0.075	0.083
E	1.15	1.35	0.045	0.053
e	0.65		0.03	
HE	2.00	2.20	0.079	0.087
L1	0.20	0.50	0.008	0.02
Lp	0.25	0.55	0.01	0.022
Q	0.10	0.30	0.004	0.012
x	-	0.10	-	0.004

DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
e1	1.55		0.06	
b2	-	0.50	-	0.02
l1	-	0.65	-	0.026

Dimension in mm/inches



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