

Chip tantalum capacitors

TCO Series P Case

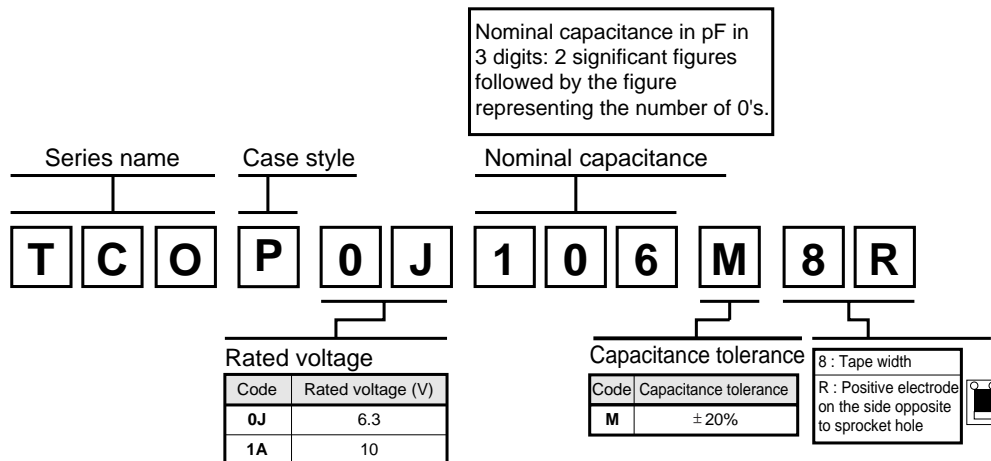
●Features (P)

- 1) Conductive polymer used for the cathode material.
- 2) Ultra-low ESR.
(1/10 compared with the conventional type)
- 3) Screening by thermal shock.

●Dimensions (Unit : mm)

(Unit : mm)	
Dimensions	P case
L	2.0±0.2
W ₁	1.25±0.2
W ₂	0.9±0.2
H	1.1±0.1
S	0.45±0.3

●Part No. Explanation



Tantalum capacitors

● **Rated Table. Marking**

TCO Series P Case

μF		Rated voltage (V.DC)	
		6.3 0J	10 1A
N	3.3		
S	4.7		*P
W	6.8		
a	10	*P	
e	15		
j	22		
n	33		
s	47		
w	68		

* Under development

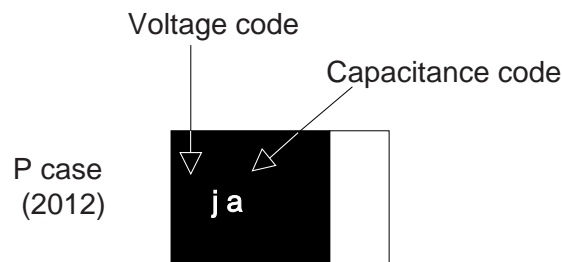
● **Marking**

The indications listed below should be given on the surface of a capacitor.

- (1) Polarity : The polarity should be shown by □ bar. (on the anode side)
- (2) Rated DC voltage : Due to the small size of P case, a voltage code is used as shown below.
- (3) Visual typical example (1) voltage code (2) capacitance code

Voltage Code	Rated DC Voltage (V)
e	2.5
g	4
j	6.3
A	10

Capacitance Code	Nominal Capacitance (μF)
A	1.0
E	1.5
J	2.2
N	3.3
S	4.7
W	6.8
a	10
e	15
j	22
n	33
s	47



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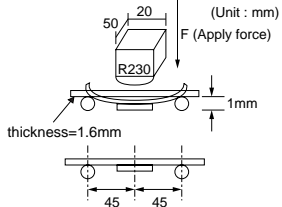
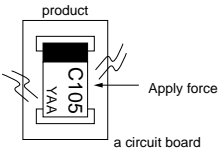
●Characteristics

Item		Performance	Test conditions (based on JIS C 5101-1 and JIS C 5101-3)
Operating Temperature		-55°C to +105°C	Voltage reduction when temperature exceeds+85°C
Maximum operating temperature with no voltage derating		+85°C	
Rated voltage (VDC)		6.3 10	at 85°C
Category voltage (VDC)		5 8	at 105°C
Surge voltage (VDC)		8 13	at 85°C
DC Leakage current		3μA or 0.1CV whichever is greater Shown in " Standard list "	Rated voltage for 5min
Capacitance tolerance		±20% Shall be satisfied allowance range.	Measuring frequency : 120±12Hz Measuring voltage : 0.5Vrms +1.5 to 2V.DC Measuring circuit : DC Equivalent series circuit
Tangent of loss angle (Df, tan δ)		Shall be satisfied the voltage on " Standard list "	Measuring frequency : 120±12Hz Measuring voltage : 0.5Vrms +1.5 to 2V.DC Measuring circuit : DC Equivalent series circuit
ESR		Shall be satisfied the voltage on " Standard list "	Measuring frequency : 100±10kHz Measuring voltage : 0.5Vrms or less
Resistance to Soldering heat	Appearance	There should be no significant abnormality. The indications should be clear.	Dip in the solder bath Solder temp : 240±5°C Duration : 10±0.5s Repetition : 1 After the specimens, leave it at room temperature for over 24h and then measure the sample.
	L.C.	Less than 150% of initial limit	
	ΔC / C	Within ±20% of initial value	
	tan δ	Less than 150% of initial limit	

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Item		Performance	Test conditions (based on JIS C 5101-1 and JIS C 5101-3)															
Temperature cycle	Appearance	There should be no significant abnormality.	Repetition : 5 cycles (1 cycle : steps 1 to 4) without discontinuation. <table border="1" data-bbox="948 456 1235 600"> <thead> <tr> <th></th> <th>Temp.</th> <th>Time</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>-55±3°C</td> <td>30±3min</td> </tr> <tr> <td>2</td> <td>Room temp.</td> <td>3min.or less</td> </tr> <tr> <td>3</td> <td>105±2°C</td> <td>30±3min</td> </tr> <tr> <td>4</td> <td>Room temp.</td> <td>3min.or less</td> </tr> </tbody> </table> After the specimens, leave it at room temperature for over 24h and then measure the sample.		Temp.	Time	1	-55±3°C	30±3min	2	Room temp.	3min.or less	3	105±2°C	30±3min	4	Room temp.	3min.or less
		Temp.		Time														
	1	-55±3°C		30±3min														
	2	Room temp.		3min.or less														
3	105±2°C	30±3min																
4	Room temp.	3min.or less																
L.C	Less than 500% of initial limit																	
ΔC / C	Within±20% of intial value																	
Df (tan δ)	Less than 150% of initial limit																	
Moisture resistance	Appearance	There should be no significant abnormality. The indications should be	After leaving the sample under such atmospheric condition that the temperature and humidity are 40±2°C and 90 to 95% RH, respectively, for 500±24h leave it at room temperature for over 24h and then measure the sample.															
	L.C	Less than 150% of initial limit																
	ΔC / C	+30% / -20%																
	Df (tan δ)	Less than 150% of initial limit																
Temperature Stability	Temp.	-55°C																
	ΔC / C	Within 0/-20% of initial value																
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "																
	L.C	-																
	Temp.	+105°C																
	ΔC / C	Within +50/0% of initial value																
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "																
L.C	Less than 1CV																	
Surge voltage	Appearance	There should be no significant abnormality.	Apply the specified serge voltage every 5±0.5 min. for 30±5 s. each time in the atmospheric condition of 85±2°C. Repeat this rocedure 1,000 times. After the specimens, leave it at room temperature for over 24h and then measure the sample.															
	L.C	Less than initial limit																
	ΔC / C	Within±20% of initial value																
	Df (tan δ)	Less than initial limit																

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Item		Performance	Test conditions (based on JIS C 5101-1 and JIS C 5101-3)
Loading at High temperature	Appearance	There should be no significant abnormality.	After applying the rated voltage for 1000^{+36} h without discontinuation via the serial resistance of 3Ω or less at a temperature of $85\pm 2^{\circ}\text{C}$, leave the sample at room temperature / humidity for over 24h and measure the value.
	L.C	Less than 200% of initial limit	
	$\Delta C / C$	Within $\pm 20\%$ of initial value	
	Df (tan δ)	150% of initial limit less than	
Terminal strength	Capacitance	The measured value should be stable.	A force is applied to the terminal until it bends to 1mm and by a perscribed tool maintain the condition for 5s.(See the figure below) 
	Appearance	There should no significant abnormality.	
Adhesiveness		The terminal should not come off.	Apply force of 5N in the two directions shown in the figure below for 10 ± 1 s after mounting the terminal on a circuit board. 
Dimensions		Refer to "External dimensions"	Measure using a caliper of JISB 7507 Class 2 or higher grade.
Resistance to solvents		The indication should be clear	Dip in the isopropyl alcohol for 30 ± 5 s, at room temperature.
Solderability		3/4 or more surface area of the solder coated terminal dipped in the soldering bath should be covered with the new solder.	Dip speed= 25 ± 2.5 mm / s Pre-treatment(accelerated aging): Leave the sample on the boiling distilled water for 1 h. Solder temp.: $245\pm 5^{\circ}\text{C}$ Duration : 3 ± 0.5 s Solder : M705 Flux : Rosin25% IPA75%
Vibration	Capacitance	Measure value should not fluctuate during the measurement.	Frequency : 10 to 55 to 10Hz/min. Amplitude : 1.5mm Time : 2h each in X and Y directions Mounting : The terminal is soldered on a print circuit board.
	Appearance	There should no significant abnormality.	

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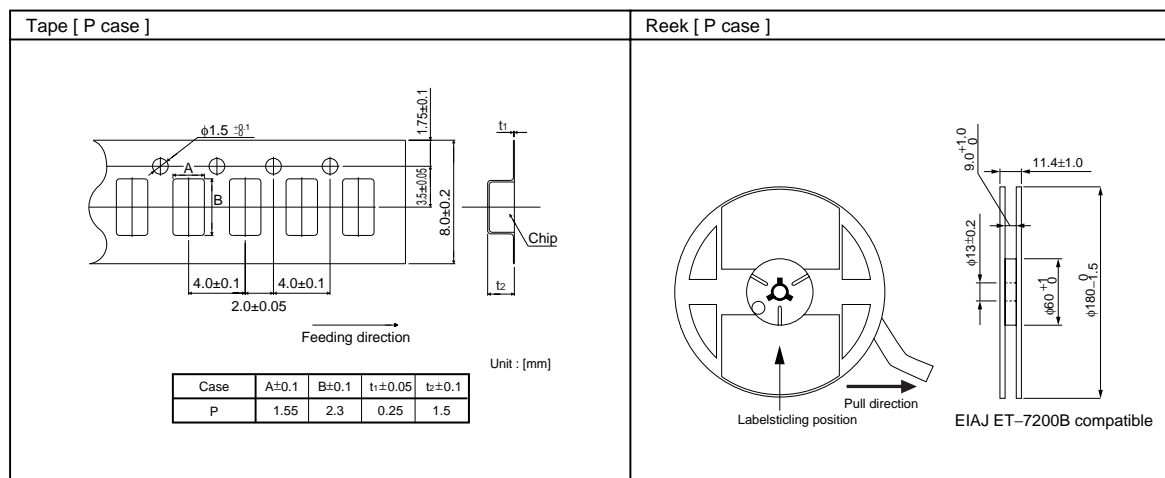
●Standard list, TCO series

< P case : 2012 size >

Part No.	Rated Voltage 85°C (V)	Category Voltage 105°C (V)	Surge Voltage 85°C (V)	Cap. 120Hz (μF)	Tolerance (%)	Leakage Current 25°C 1WV 5min (μA)	Df 120Hz (%)			ESR 100kHz (mΩ)
							-55°C	25°C 85°C	105°C	
TCO P 0J 106 □	6.3	5	8	10	±20	6.3	6	6	9	500
TCO P 1A 475 □	10	8	13	4.7	±20	4.7	6	6	9	500

□=Tolerance(M : ±20%)

●Packaging specifications

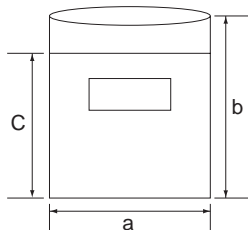


●Packaging style

Case code	package	Packaging style		Symbol	Basic ordering units
P	Taping	plastic taping	φ180mmReel	R	3,000pcs

● Damp proof package

- ① One reel is packed in aluminum bag.
The size of aluminum bag is 240(g) x 250(b)mm.
The size up to 230(c)mm is to zipper.
- ② A desiccant is packed with a reel.
- ③ The aluminum bag is heat-sealed.
- ④ The label of the same as the label on the reel is placed on the aluminum bag.



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● Electrical characteristics and operation notes

(1) Leakage current-to-voltage ratio

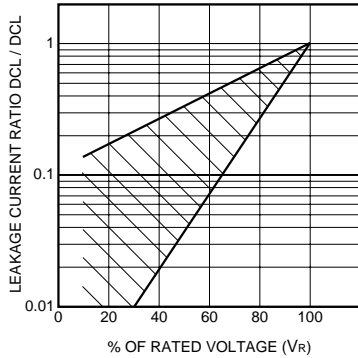


Fig.1

(2) Derating voltage as function of temperature

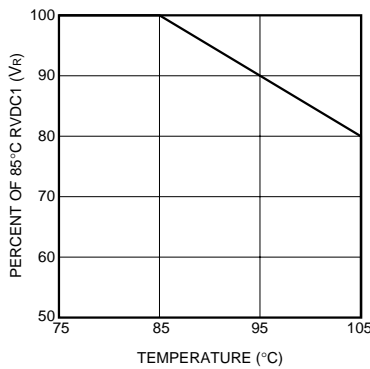


Fig.2

85°C		105°C
Rated Voltage (V.DC)	Surge Voltage (V.DC)	Category Voltage (V.DC)
6.3	8	5
10	13	8

(3) Reliability

The malfunction rate of tantalum solid state electrolytic capacitors varies considerably depending on the conditions of usage (ambient temperature, applied voltage, circuit resistance).

Formula for calculating malfunction rate

$$\lambda_p = \lambda_b \times (\pi_E \times \pi_{SR} \times \pi_Q \times \pi_{CV})$$

- λ_p : Malfunction rate stemming from operation
- λ_b : Basic malfunction rate
- π_E : Environmental factors
- π_{SR} : Series resistance
- π_Q : Level of malfunction rate
- π_{CV} : Capacitance

For details on how to calculate the malfunction rate stemming from operation, see the tantalum solid state electrolytic capacitors column in MIL-HDBK-217.

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(4) Impedance frequency characteristics

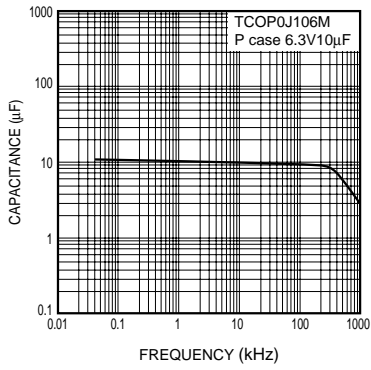


Fig.3

(5) ESR frequency characteristics

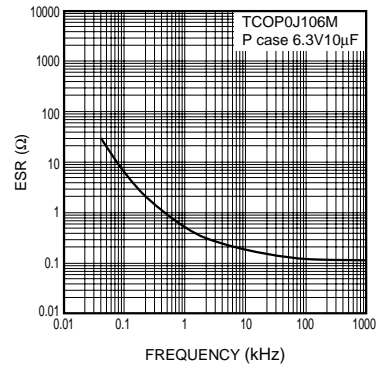


Fig.4

(6) Capacitance temperature characteristics 120Hz

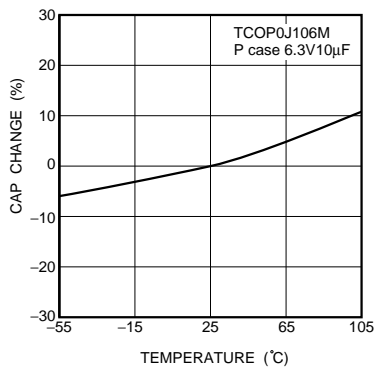


Fig.5

(7) ESR temperature characteristics 100kHz

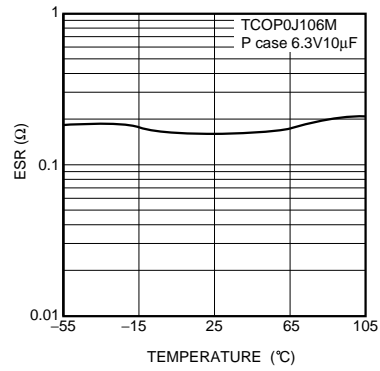


Fig.6

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